

<b>Notice of References Cited</b>	Application/Control No. 09/991,202	Applicant(s)/Patent Under Reexamination GOPINATH ET AL.	
	Examiner Stanetta D. Isaac	Art Unit 2812	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-6,797,652	09-2004	Ngo et al.	438/687
	C	US-6,159,871	12-2000	Loboda et al.	438/786
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	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Stanley Wolf and Richard N. Tauber, Silicon Processing For The VLSI Era, Volume I, Lattice Press, 1986, pages 520-535
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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